

# **Notice of References Cited**

Application/Control No.  
**09/822,532**

Applicant(s)/Patent Under Reexam  
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Examiner  
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Art Unit  
**1765**

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## **U.S. PATENT DOCUMENTS**

	Document Number Country Code-Number-Kind Code	Date MM-YYYY <sup>1</sup>	Name	Classification <sup>2</sup>	
A	6,181,569	1/2001	Chakravorty	361	761
B	5,976,710	11/1999	Sachdev et al.	428	620
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## **FOREIGN PATENT DOCUMENTS**

	Document Number Country Code-Number-Kind Code	Date MM-YYYY <sup>1</sup>	Country	Name	Classification <sup>2</sup>	
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O						
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R						
S						
T						

## **NON-PATENT DOCUMENTS**

	Include, as applicable: Author, Title, Date, Publisher, Edition or Volume, Pertinent Pages
U	S. Wolf, "Silicon Processing for the VLSI Era", Lattice Press, Volume 1, 1986, pp. 589.
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<sup>1</sup> A copy of this reference is not being furnished with this Office action. See MPEP § 707.05(a).

<sup>1</sup> Dates in MM-YYYY format are publication dates.

<sup>2</sup> Classifications may be U.S. or foreign.